

XXV Symposium „Photonics and Web Engineering”

FE&IT WUT, 29-30 January 2010

Agenda

2010-01-29 Day 1

16:00 Opening speech,
Prof. R.Romaniuk

Medical Session - prof. J. Mulawka

16:15 (filler)

16:30 Computer-assisted detection of epileptiform centers on SPECT images
- D.Grzegorzczak, D Dunin-Wąsowicz, J.J.Mulawka

16:45 Parameter selection for GGI algorithm for different patient groups -
K.Kocel, P.Wąsiewicz, M.Syczewska

17:00 Young addicted men hormon profile detection - P.Wąsiewicz,
P.Zieliński, B.Leszczynska, J.Gromadzka-Ostrowska

17:15 Implementation of expert system in Prolog - E.Tataj, D.Dunin-
Wąsowicz, J.J.Mulawka

17:30 Analysis of clusters in veterinary medicine - P.Wąsiewicz, P.Zieliński,
P.Przybylski

17:45 coffee break

PERG session 1

18:00 Multi parametric signal analyzer for radiation spectrometry - Stefan
Korolczuk

18:15 (filler)

2010-01-30 Day 2

PERG session 2

10:00 Virtual Measurement Systems - Maciej Lipinski

10:15 Modeling of Object Oriented Hardware - mgr inż. Paweł Drabik

10:30 Multichannel Acquisition System with Photomultiplier Detectors -
Michał Bohdanowicz

10:45 Front-end electronic for CCD camera the SPEKTROP project. - Piotr
Sitek

11:00 digital video signal processing chain for "Pi of the Sky" experiment -

Piotr Obroślak

11:15 coffe break

PERG session 3

11:30 Universal Measurement System for Monitoring the Environment -
Wojciech Górecki

11:45 Parametrization and configuration of the embedded data acquisition
system in FPGA - Agnieszka Zagoździńska

12:00 Invited speech - PALINDROMY Prof. Tadeusz Morawski

12:30 Lunch break

13:00 PERG Management System - Kamil Maj

PERG session 4

14:30 New VHDL implementation of the receiver board for the Safe Machine
Parameters System - Maciej Kwiatkowski

14:45 Universal Measurement Card - Łukasz Kolator

15:00 Documentation generator for C/C++, VHDL and Matlab source codes.
- Bartłomiej Nitoń

15:15 Quadrocopter - flying autonomic platform - Paweł Poławski

15:30 The 3D Laser Scanner System - Tomasz Kuźma

15:45 UML graph generator for VHDL source codes. - Bartłomiej Nitoń

16:00 coffee break

ISYP session

16:15 Przetwarzanie obrazów - generacja współrzędnych plamki świetlnej na powierzchni czujnika CMOS - Łukasz Romanowski

16:30 Front-end of eight channel oscilloscope – electrical schematic and PCB layout - Piotr Kolasinski

16:45 License plate identification system - Mikołaj Bryl

17:00 something about myself - Kamil Borowski

17:15 Transmission of measurement data - Implementation in Java for Component Internal Interface - Piotr Romanowski

17:30 Power supply unit for photomultiplier - Radosław Rybaniec Tomasz Janicki

17:45 Short presentation on myself - Michał Lorenc

18:00 The Front-end driver of the Data Acquisition in the Totem experiment - Adrian Fiergolski